

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization International Bureau



(43) International Publication Date  
29 April 2004 (29.04.2004)

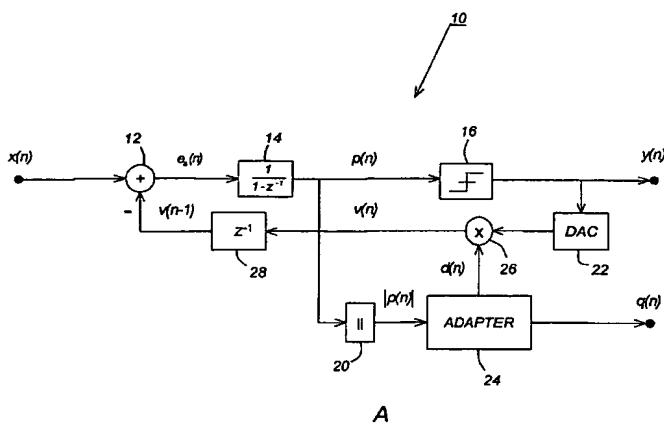
PCT

(10) International Publication Number  
WO 2004/036754 A3

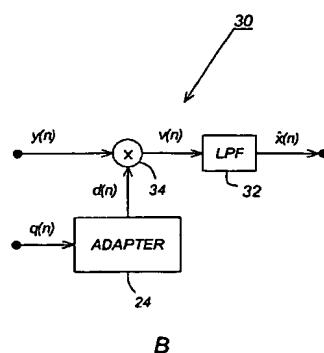
- (51) International Patent Classification<sup>7</sup>: H03M 03/00, 07/30, 07/34, H04B 14/06
- (21) International Application Number: PCT/US2003/032835
- (22) International Filing Date: 15 October 2003 (15.10.2003)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:  
60/418,644 15 October 2002 (15.10.2002) US
- (63) Related by continuation (CON) or continuation-in-part (CIP) to earlier applications:  
US 10/256,606 (CIP)  
Filed on 27 September 2002 (27.09.2002)  
US 10/332,750 (CIP)  
Filed on 13 January 2003 (13.01.2003)
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- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK,

*[Continued on next page]*

(54) Title: ADAPTIVE MULTI-BIT DELTA AND SIGMA-DELTA MODULATION



(57) Abstract: An adaptive multi-bit delta and sigma-delta modulation (10) and demodulation technique, wherein a one-bit modulator generates a binary output signal from an analog input signal and a multi-bit adapter (24) for generating a scaling signal for scaling a step-size of the sigma-delta modulator.





MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) **Designated States (regional):** ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

**Declaration under Rule 4.17:**

- *of inventorship (Rule 4.17(iv)) for US only*

**Published:**

- *with international search report*
- *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*

(88) **Date of publication of the international search report:**

24 June 2004

*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

# INTERNATIONAL SEARCH REPORT

International application No.

PCT/US03/32835

## A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : H03M 03/00, 07/30, 07/34; H04B 14/06  
 US CL : 341/51, 76, 143; 375/244, 247

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 341/51, 76, 143; 375/242-254

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched  
 NONE

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)  
 NONE

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 6,339,621 B1 (COJOCARU ET AL) 15 JANUARY 2002 (15/01/02), SEE THE ABSTRACT.	1-14

Further documents are listed in the continuation of Box C.

See patent family annex.

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"&"

document member of the same patent family

Date of the actual completion of the international search

10 March 2004 (10.03.2004)

Date of mailing of the international search report

05 MAY 2004

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